





Embedded Systems Security

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Previously

- PhD from INRIA (Grenoble)
- Postdoc ETH Zürich

Assistant professor at EURECOM

• Since 2011

Eurecom

- Petite école d'ingénieurs a Sophia Antipolis
- Membre de l'institut mines-télécom

2/3 d'étudiants étrangers

A un nouveau diplôme cette année

• Diplôme d'ingénieur de spécialisation (CTI)

Embedded Systems Security

This is a very wide topic, what we will see today

- Basics in embedded systems security, hardware attacks
- Example of an embedded system software compromise
- How to analyze such systems
 - Example of static and dynamic analysis

Embedded devices diversity

SmartCards



Connected devices



0

2

Sensors







72

Industrial systems

'-'



SIEMENS

There are security devices

Devices that are designed for a security application

- Well defined purpose, often custom
- Limited attack surface
- High level of security achievable
- Not (too) cost sensitive, volume production





There are security devices

► We know how to do security for a while...



There are 'just' devices

Those embedded devices are everywhere

- Not security devices, security not their main purpose
- Often COTS devices

Sometimes availability and/or safety is important

- But often no real need for security
- Lack of a clearly identified adversary
- Resistant to random faults
- Attacker will trigger improbable conditions

Your computer is made of many "computers"



What could happen when one is compromised?

Internet of what ?

MIT Technology Review

BUSINESS REPORT

The Internet of Things

Billions of computers that can sense and communicate from anywhere are coming online. What will it mean for business?

CONTENTS

The Big Question

The Economics of the Internet of Things

The Lowly Thermostat, Now Minter of Megawatts

The Light Bulb Gets a Digital Makeover

GE's \$1 Billion Software Bet

The Internet of You

Silicon Valley to Get a Cellular Network, Just for Things

Plus: Industry resources, key executives, and companies to watch

A single match for "security"

George Arnold

Director, Standards Coordination Office, and national coordinator, smart grid interoperability, National Institute of Standards and Technology Gaithersburg, Maryland

On the Internet of things, standards and protocols will determine winners and losers. That makes George Arnold a quietly important player. As a smart-grid czar at the federal National Institute of Standards and Technology, Arnold is involved in setting standards for how digital technologies will affect the politically and strategically important electricity grid and

other "cyber physical" systems. As part of an informal industry group known as the Kitchen Cabinet, Arnold was influential in establishing the commercial Industrial Internet Consortium this year. NIST's 2014 budget request included \$18.8 million to study cyber-physical systems and their security. Arnold is a veteran of Bell Labs and the coauthor of one of the earliest examples of chess-playing software.

Is there really a problem with security in IoT?

Before IoT

► Video Protection



Before IoT

Video Protection Surveillance



IoT: composition







Composition Kills

Slide Courtesy of T. Goodspeed

← → X 🗅



We just "I"-ified a camera

What happened?

- Attack surface increased
- Devices not designed for security now online
- Threat model changed
- Value increased just by being online
- Deploy and forget

Challenges

Challenges

Embedded devices not immune anymore to compromise

- Security devices are rather secure
- But we rely on *non security devices* for our daily security and privacy

But embedded devices are obscure, hidden

- You know the OS you are running on your server/laptop and if it is up to date
- But when was the last update of your "fridge's" OS?

Challenges

Networking/wireless interfaces are increasing the attack surface

- IoT=> IoIT? Is it the Internet of the Insecure Things ?
- Remote, software attacks become possible

In general difficult to know the runtime status of a device

Sophisticated attacks previously demonstrated

• Hundreds of thousands of devices compromised

So what can we do about this ?

- Good design Security Development Lifecycle (SDL) Hardening devices

Raise public awareness

Evaluate the impact of compromises

Develop tools and techniques to improve security evaluation at low cost

Making devices more robust Hardware hardening

Different possible attacks

Simple hardware attacks are very simple!

- Identify the chips on the device
- Search for the datasheets of the chips
- Trace the PCB, connections between the devices
- Identify the purpose of each chip on the device
- Find jtag, serial, i2c ...
- Dump memories, (serial flash !)
- Try to obtain the software, access internal secrets, execute code...



Probing bus, following vias



- Needles and a multimeter to follow the vias
- Alternative, desolder all components and take picture
- For more see Joe Grand woot'14 paper

Bus pirate



- Once a port is found we need to read it
- An universal device for talking many simple protocols

Jtagulator



 Automate detection of JTAG or serial ports

Goodfet



- Similar to buspirate
- Initially designed to be a cheap JTAG
- Supports many protocols, flash memories...
- Easily extensible firmware
- Cheap PCB available!
- Good to learn

Many other more advanced devices

- Good oscilloscope
- Logic analyzers
- ...

Soldering skills help



Simple countermeasures

Hardware countermeasures

- Obfuscate labels
- Bury important traces into deep layers
- Use BGA (harder to tap and desolder)
- Epoxy on top of chips
- Don't connect JTAG, serial or debug ports
- Use chips with internal memories
- Encrypt memory bus
- ...

Attacking the hardware

When an attacker can access the hardware a lot of things can be done

- Digging secrets in hardware
- Tampering internal signals and memories

Attacking the hardware directly



Decaping chips



ROM memory form an MSP430


ROM memory form an MSP430



Other attacks

- Fault attacks
 - Make some logic fail, e.g., interpret incorrectly CPU instructions
 - Very efficient attacks
- Most common are
 - Clock, voltage Glitching
 - Laser fault

Functionnal chip



Probing



Hardware Countermeasures



- Mesh of wires above the logic
 - Tamper detection
- Light sensors
- Internal clock
- Glitch detection

• Etc...

Hardware attacks cont. (a.k.a. bazooka)



- A Focused Ion Beam can be used to modify a chip
- This is very powerful
- Quite expensive equipment
- What is the right level of security for the device ?

Picture: "Fib" by english User:Cm the p - English version. Licensed under CC BY-SA 3.0 via Wikimedia Commons - https://commons.wikimedia.org/wiki/File:Fib.jpg#mediaviewer/File:Fib.jpg Hardware attacks countermeasures

- Additional metal layers
- Side channel/fault resistance
- This is smartcard level security
- Very few embedded devices have this level of protection
 - Has a cost

So what can we do about this ?

Good design

- Security Development Lifecycle (SDL)
- Hardening devices

Raise public awareness
Evaluate the impact of compromises

Develop tools and techniques to improve security evaluation at low cost

Example of a software attack

Implementation and Implications of a Stealth Hard-Drive Backdoor

ACSAC 2013 (Best Student Paper Award)



Jonas Zaddach Davide Balzarotti Aurélien Francillon



Erik-Oliver Blass

Travis Goodspeed





Anil Kurmus Ioannis Koltsidas



Moitrayee Gupta

Goals

It's about threat models !

- Do we care about "hardware" compromises ?
- Is it practical, feasible ?
- An example attack would be to
 - Understand, then backdoor the firmware
 - Malware compromises OS
 - Updates HDD with malicious firmware update
 - Disk is formatted, OS "re-installed"
 - But malicious HDD firmware remains!
 - OS compromised again from on the next boot



Reverse engineering approach

Study PCB

- Chips : DRAM, Serial flash, JTAG (deactivated?), motor controller
- Google model ! Data recovery services.
- Firmware updates available but format obscure A serial diagnostic menu is available from M/S pins
 - Gives PEEK/POKE primitives
 - Allows to dump memory/load code
- A serial Flash is on PCB
 - Contains 2nd bootloader
 - Could be desoldered/dumped/changed

Reverse engineering approach tools

- Main task is to understand the firmware
 - But it's very large and obscure...
 - We need a way to debug the running firmware
 - To hook the backdoor in the original code
- Ida pro, a lot of patience,
 - Custom tools, dynamic analysis

Device instrumentation



Architecture



Backdoor Implementation

Many technical difficulties...

- Custom, event based OS
- Large statically linked code, no symbols

Results:

- Backdoor inserted in a firmware update
- Intercepts disk writes
- Can read blocks from disk (unstable*)
- No significant overhead (1%)

Exfiltration exemple: an online forum



Example: Exfiltrating a sensitive file

Use HDD as remote block device

- We can request any block
- So we can "mount" partitions

Exfiltrate /etc/shadow in nine "queries":

- First retrieve partition table in MBR
- Then superblock of ext3 partition

• ...

Total time: < 1 minute

In summary

We reverse engineered and backdoored a COTS drive

- 10 person-month effort
- Without any privileged information
- No significant performance overhead

Data-exfiltration backdoor

- No cooperation from host
- Stealthy

So is this a realistic threat model after all ?

• IRATEMONK

IRATEMONK (12/2013) ?

TOP SECRET//COMINT//REL TO USA, FVEY **IRATEMONK** ANT Product Data (TS//SI//REL) IRATEMONK provides software application persistence on desktop and laptop computers by implanting the hard drive firmware to gain execution 06/20/08 through Master Boot Record (MBR) substitution. SLICKERVICAR WISTFULTOLL in SERUM 0 OIM / JMSQ Requests and SEAGUILLEARC R&T Analyst ROC 1 High S 1 m ROC CDR I Target A INITEDRAKE UNITEDRAKE Server (TS//SI//REL) IRATEMONK Extended Concept of Operations (TS//SI//REL) This technique supports systems without RAID hardware that boot from a variety of Western Digital, Seagate, Maxtor, and Samsung hard drives. The supported file systems are: FAT, NTFS, EXT3 and UFS. (TSI/SI//REL) Through remote access or interdiction, UNITEDRAKE, or STRAITBAZZARE are used in conjunction with SLICKERVICAR to upload the hard drive firmware onto the target machine to implant IRATEMONK and its payload (the implant installer). Once implanted, IRATEMONK's frequency of execution (dropping the payload) is configurable and will occur when the target machine powers on. Status: Released / Deployed. Ready for Unit Cost: \$0 Immediate Delivery POC: S32221 @nsa.ic.gov Derived From: NSA/CSSM 1-52 Dated: 20070108 Declassify On: 20320108

TOP SECRET//COMINT//REL TO USA, FVEY

Internship? (1/2015)

(TS//SI//REL) Integrate SSD research into IRATEMONK products. This will involve 4 different parts:

- (TS//SI//REL) Leveraging research to create ARM-based SSD implant. This works involves reverse engineering SSD firmware and creating C and ARM assembly code to place inside of a firmware image to implement the IRATEMONK algorithm.
- (TS//SI//REL) Create version of the IMBIOS code that supports the SSD implant. This code runs on the x86 host and involves writing both C and

nternProjects - WikiInfo

x86 assembly. This work will involve interacting with the firmware implant as well as the code that IMBIOS bootstraps (SIERRAMIST).

- (TS//SI//REL) Add support for the SSD to WICKEDVICAR.
 WICKEDVICAR is the remote tool used to perform remote survey and installation. This code is C++ and will involve interacting with the firmware implant from a Windows OS.
- (TS//SI//REL) Add the SSD vendor support to the IRATEMONK firmware

First malware sample found by Kaspersky (2/2015)

10. What is the most sophisticated thing about the EQUATION group?

Although the implementation of their malware systems is incredibly complex, surpassing even Regin in sophistication, there is one aspect of the EQUATION group's attack technologies that exceeds anything we have ever seen before. This is the ability to infect the hard drive firmware.

We were able to recover two HDD firmware reprogramming modules from the EQUATIONDRUG and GRAYFISH platforms. The EQUATIONDRUG HDD firmware reprogramming module has version 3.0.1 while the GRAYFISH reprogramming module has version 4.2.0. These were compiled in 2010 and 2013, respectively, if we are to trust the PE timestamps.

NSA approach to backdooring Disks

From public documents we know that:

- Compromise a computer
- A DLL loads a modified firmware image
- The firmware replaces the MBR by a modified MBR
- Most likely then use this to infect the boot chain
 - Not very stealth !
 - Could be detected by a TPM ?

Snowden documents on "interdiction"



(TS//SI//NF) Left: Intercepted packages are opened carefully; Right: A "load station" implants a beacon

(TS//SI//NF) In one recent case, after several months a beacon implanted through supplychain interdiction called back to the NSA covert infrastructure. This call back provided us access to further exploit the device and survey the network.

What can we do about this ?

So what can we do about this ?

Good design

- Security Development Lifecycle (SDL)
- Hardening devices

Raise public awareness

• Evaluate the impact of compromises

Develop tools and techniques to improve security evaluation at low cost

Performing analysis of embedded systems

- 2 main ways to analyze embedded systems:> Static analysis
- Dynamic Analysis

We consider only black (or grey) box analysis
Also realistic for manufacturers, audits,...

AVATAR: A Framework for Dynamic Security Analysis of Embedded Systems' Firmwares

Presented at NDSS '14

Jonas Zaddach,

Luca Bruno, Aurélien Francillon, Davide Balzarotti



Dynamic analysis

Problem:

- Unknown peripherals
- Emulating CPU only not sufficient
- Limited visibility with execution on hardware
 - With a gdb stub, JTAG...

Advanced analysis impossible • Tracing, Tainting, Symbolic execution

Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
 - Fuzzing
 - Tainting
 - Symbolic Execution



Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
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 - Symbolic Execution

Collecting an execution trace



Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
 - Fuzzing
 - Tainting
 - Symbolic Execution

Testing with random input



Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
 - Fuzzing
 - Tainting
 - Symbolic Execution

Data flow tracking



Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
 - Fuzzing
 - Tainting
 - Symbolic Execution

Multipath exploration



Techniques that are typically used on a PC

- Advanced debugging techniques
 - Tracing
 - Fuzzing
 - Tainting
 - Symbolic Execution
- Integrated tools
 - IDA Pro
 - GDB
 - Eclipse


Avatar

Avatar idea :

- Arbitration framework
- Emulate the firmware on the emulator
 - Forward IO to deivce
- Python scripting of all tools (qemu, jtag, symbolic execution...)
- A process, helps for reverse engineering at the same time as automating many tasks and testing
- Open source, with examples: http://s3.eurecom.fr/tools/avatar/















Use cases

Analyzing the ROM bootloader of an HDD

- Finding bugs in a Zigbee wireless sensor device
- Analyzing the baseband code of a GSM feature phone







Dynamic analysis is great, but does not scale. How can we analyze such systems at a large scale ?

- Routers
- Printers
- ► VoIP
- Cars
- Drones

www.devttys0.com/2013/10/reverse-	engineering-a-d-link-backdoor/
Based on the source code of the	HTML pages and some Shodan search results
D-Link devices are likely affected	:
DIR-100	
DIR-120	
DI-624S	
DI-524UP	
DI-604S	
DI-604UP	
DI-604+	
TM-G5240	\sim

- BRL-04R
- BRL-04UR
- BRL-04CW

You stay classy, D-Link.

- Routers
- Printers
- ► VoIP
- Cars
- Drones



BRL-0

You stay classy, D-Link.



- Routers
- Printers
- ► VoIP
- Cars
- Drones



- Routers
- Printers
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- Cars
- Drones



- Routers
- Printers
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- Drones



- Routers
- Printers
- ► VoIP
- Cars
- Drones



Problem:

- Those are individual, manual, tedious efforts
- How to do this at large scale?

The problem with large scale analysis

- Heterogeneity of
 - Hardware, architectures, OSes, users, requirements, security goals
- Manual analysis does not scale, it requires
 - $^{\circ}$ Finding and downloading the firmwares
 - Unpacking and performing initial analysis
 - (Re-)discovering the same or similar bugs in other firmwares

Our approach

Collect a large number of firmware images
Perform broad but simple static analysis
Correlate across firmwares

Many advantages:

- No intrusive online testing, no devices involved
- Scalable

But also many challenges

« A Large Scale Analysis of the Security of Embedded Firmwares » Andrei Costin, Jonas Zaddach, Aurélien Francillon, Davide Balzarotti **USENIX Security** 2015

Challenges

- Firmware identification (.exe/.ps/...)
- Firmware Unpacking
- Representative dataset
- Scalability, computational limits
- Results confirmation

Clearly a Firmware







• E.g., upgrade by printing a PS document

XEROX	CentreW Internet Phaser	/are Services 6250		Printer Neighborhood	Index	? Help
<u>Status</u>	Jobs	Print	Properties	Suppor	1	
Name: kevin DNS: kevin supp xerox P: 13.62	d6250 d6250. ort.office. .com 2.70.247 250	File Down Select a print-r press the print	load eady file (PostS butter	cript, PDF, PCL (or Plain Text) a	ind
E Print S	aved Jobs	Print Series		Diowse		
File Do	e Saved Jobs wnload	Print Imm	ediately			
Print D	emo Pages	Saved Print	nt			
Print C	onfiguration Pag	Job Name	e:			
			(

Challenge: Unpacking & Custom Formats

- How to reliably unpack and learn formats?
- E.g., vendor provides a .ZIP 'firmware package'
 - .ZIP → .EXE+.PS
 - $.EXE \rightarrow self-extracting archive$
 - Extract more or not?
 - Turns out to contain a printer driver inside
 - .PS \rightarrow ASCII85 stream \rightarrow ELF file that could be:
 - A complete embedded system software
 - An executable performing the firmware upgrade
 - A firmware patch
- Often, a firmware image \rightarrow just 'data' binary blob









Unpacking

759 K total files collected
Filter non firmware

172 K filtered files (firmware candidates)

Random selection

- ► 32 K firmwares analyzed
 - Unpack attempt
- 26 K firmwares unpacked (fully or partially)

Files extraction

1.7 M files after unpacking

OS in our dataset



63 %ARM, 7 % Mips, 86 % Linux 7 % VxWorks/Nucleus RTOS/Windows CE









RSA Keys

SSL keys correlation



RSA Keys Private RSA kevs SSL keys correlation Analysis & Reports Database vulnerability propagation VendorC Device1 • A Priva 1 RSA private key: **HTTPS Ecosystem Scans** 30,000 vulnerable zmap devices online Check ZMap IP addresses
RSA Keys Private RSA kevs SSL keys correlation Analysis & Reports Database vulnerability propagation VendorC Device1 1 RSA private key: **HTTPS Ecosystem Scans** 30,000 vulnerable zmap devices online Check ZMap Not all the same IP addresses brand SAME private RSA SAME self-signed SSL certifica **DIFFERENT** vendor

Device2

RSA Keys Private RSA keys SSL keys correlation Analysis & Reports Database vulnerability propagation VendorC Device1 1 RSA private key: **HTTPS Ecosystem Scans** 30,000 vulnerable zmap devices online Vulnerable Check ZMap Components Not all the same IP addresses brand SAME private RSA SAME self-signed SSL certificate **DIFFERENT** vendor

Device2

Another example of composition failure: Fireworks!

- Replacing wires by wireless in a system
- Lack of security
- Anyone can control the fireworks

 Fortunately firmware updates possible and now deployed



www.firmware.RE (beta)



Results: Summary

- ► 38 new vulnerabilities (CVE)
- Correlated them to 140 K vulnerable online devices
- Affected 693 firmware files by at least one vulnerability

See our Usenix Security 2014 paper

Next steps

- Rather simple analysis so far
- We are now working on
 - Doing smarter analysis on this dataset
 - Improving the dataset

Take away message

Saltzer and Schroeder (1975)

Few of the principles from Saltzer and Schroeder (a.k.a. the basics):

- Economy of Mechanism ("KISS")
- Fail-safe defaults
- Open design
- Separation of privilege
- Least privilege
- Psychological acceptability

Wrong Assumptions

- Forget things like "It will never be attacked because it is:
 - Stripped, binary only
 - Firmware is not on the Internet
 - Hardware is not documented
 - We disabled JTAG
 - •

Frequently found problems in firmware

- Updates with old software (release/compilation date)
- Default passwords
- SSL private keys
- SSH keys (authorized_keys)
- Debug access a.k.a. backdoors...
- Web vulnerabilities
- Building Images as root
- Packaging Outdated and Vulnerable Software

Defending / obfuscation (by far not an exhaustive list!)

- Plan for updates automated and secure
- Clean default passwords, keys
- Implement countermeasures (NX/Canaries/ASLR...)
- Secure boot, signed updates
- (contradictory) Please don't lock the user out!
- Obfuscating firmware
 - Remove strings, strip symbols
 - Make firmware hard to obtain (encrypted)
 - Careful key management
 - Make it hard to analyze hardware

Security Trade-off

Security is hard

Costs money, Time, manpower

Features are selling points

- More Features, more attack surface and less security
- Less features, higher cost, latter
 - market failure ?
- Long term v.s. short term thinking



Conclusion

A lot of poorly secured devices produced

- But bad cases more visible
- Security is hard and expensive, we need
 - Public/customer awareness
 - Security Standards?
 - Independent security audit
 - Automated Firmware updates!

Diverse and powerful adversaries

No such thing as total security

• But there should be a minimal level of security

References

- Bunnie Huang blog: hardware attack
 - Hacking the PIC 18F1320 http://www.bunniestudios.com/blog/?page_i d=40
 - Hacking the Xbox An introduction to Reverse engineering (free ebook!)
- http://travisgoodspeed.blogspot.com
- http://siliconpr0n.org/
- http://zeptobars.ru/
- Sergei Skorobogatov work http://www.cl.cam.ac.uk/~sps32/

References

- Oliver Kömmerling, Markus G. Kuhn: Design Principles for Tamper-Resistant Smartcard Processors, Proceedings of the USENIX Workshop on Smartcard Technology, 1999
- Printed Circuit Board Deconstruction Techniques, Joe Grand, WOOT '14
- Firmware unpacking tools BAT and Binwalk
- Talk about hardware attacks on secuer chips "Hardware reverse engineering tools" Olivier Thomas, Recon 2013 https://www.youtube.com/watch?v=o77GTR8RovM

Some of our projects

Avatar Project :

- http://s3.eurecom.fr/tools/avatar/ Firmware.re:
- http://www.firmware.re/

Our publications can be found here : http://www.s3.eurecom.fr/publications.html

Questions ?

Some of the people working on this:



Jonas Zadach



Luca Bruno



Andrei Costin



Davide Balzarotti